

FIG. 1A

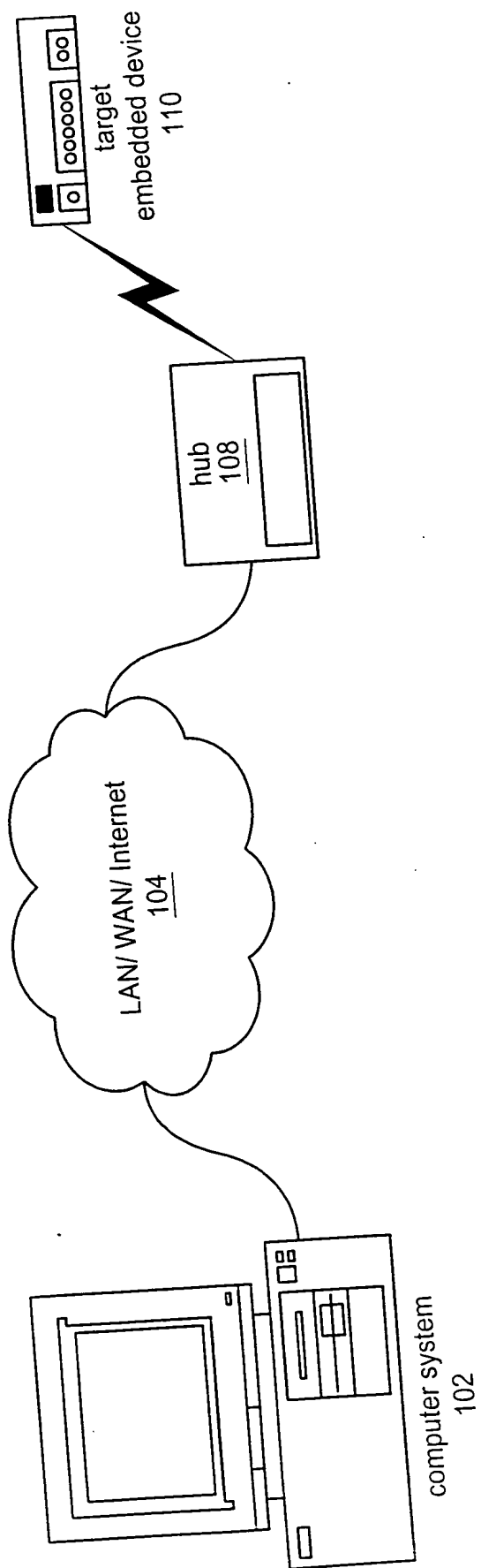


FIG. 1B

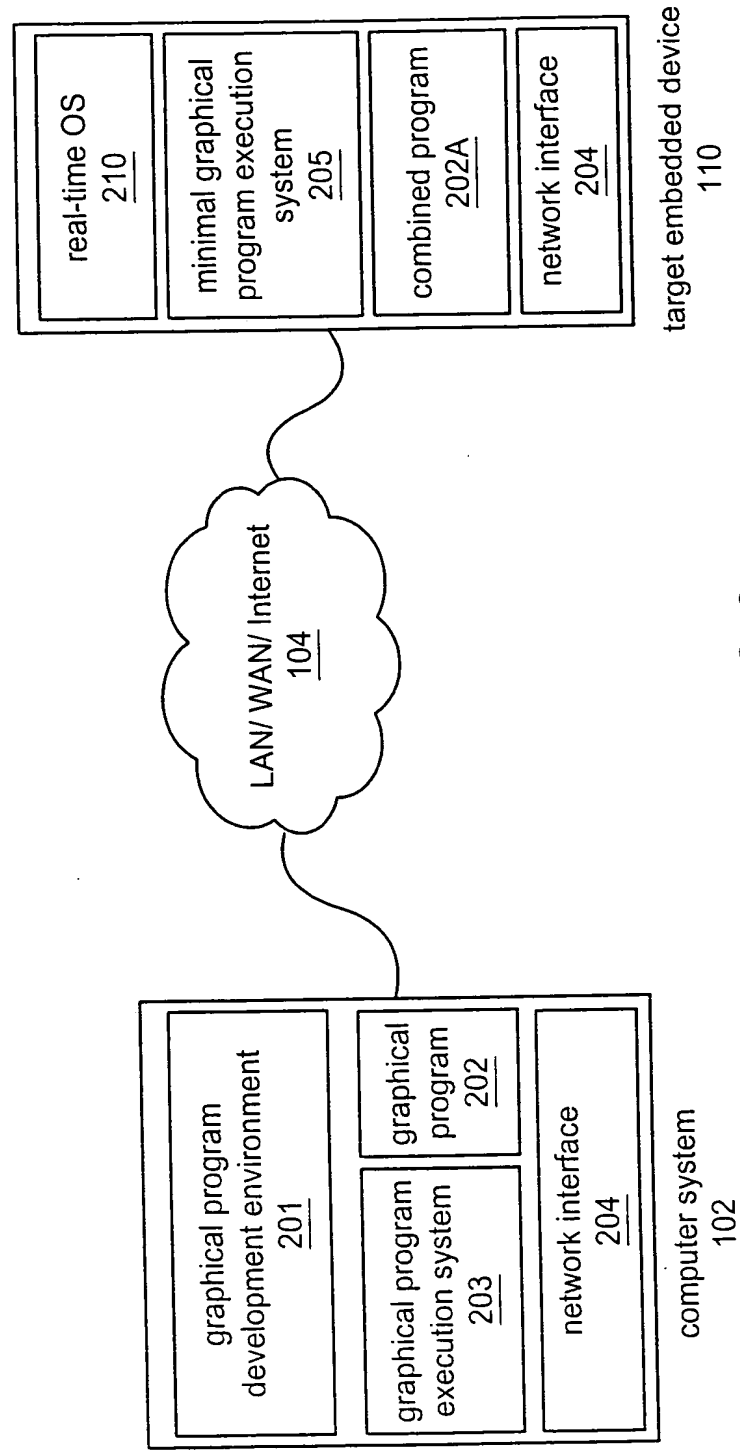


FIG. 2

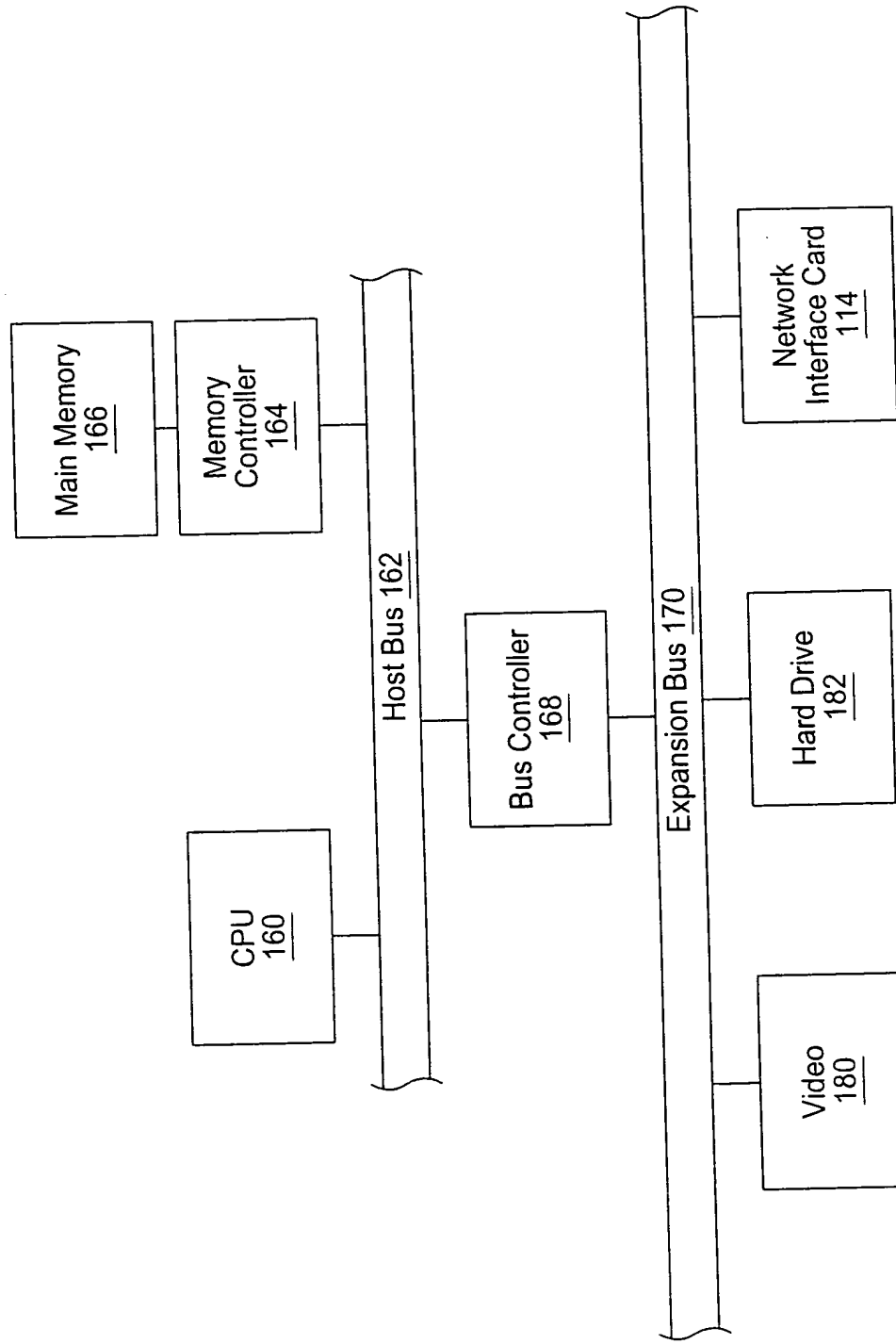


FIG. 3

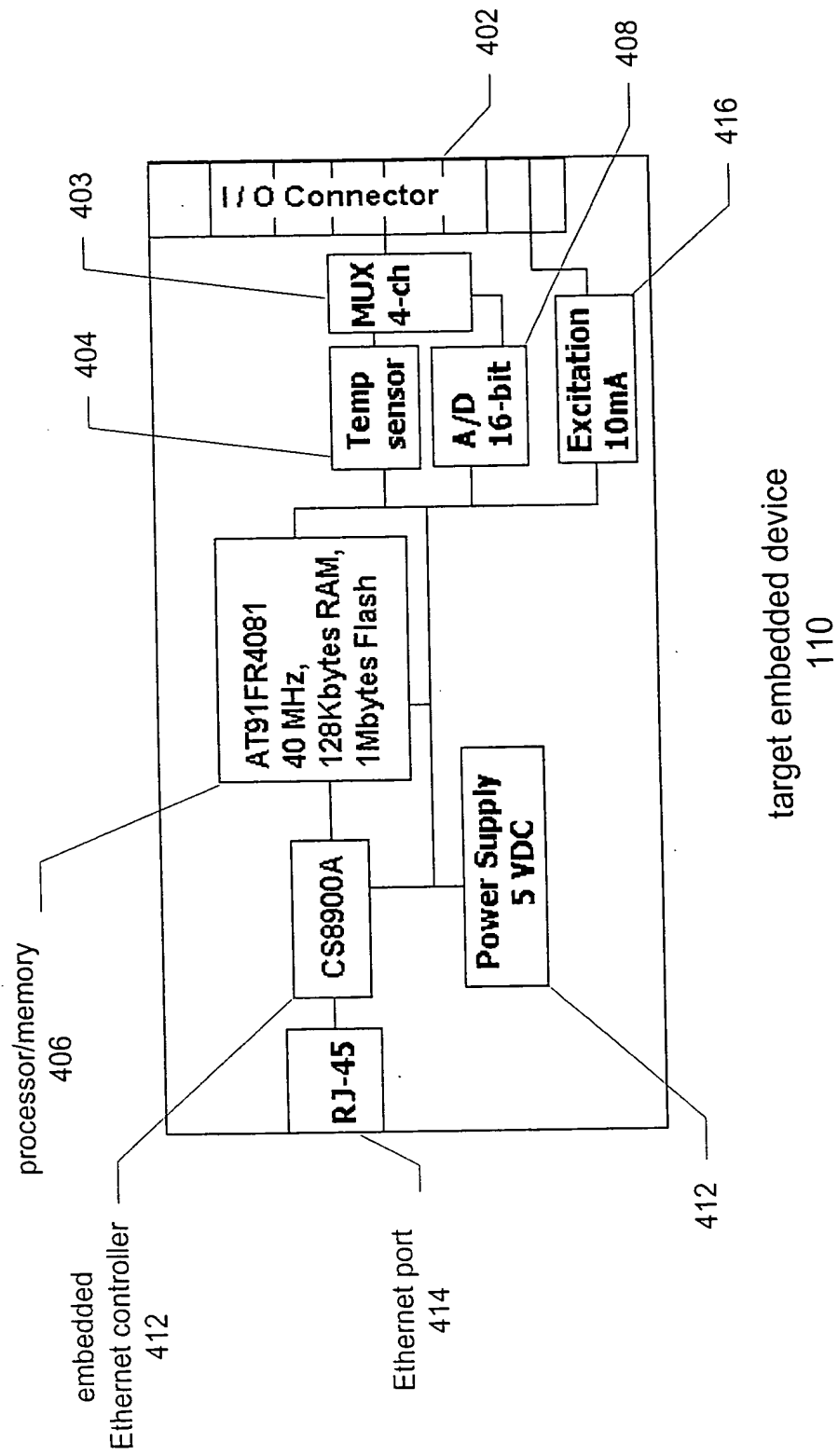


FIG. 4A

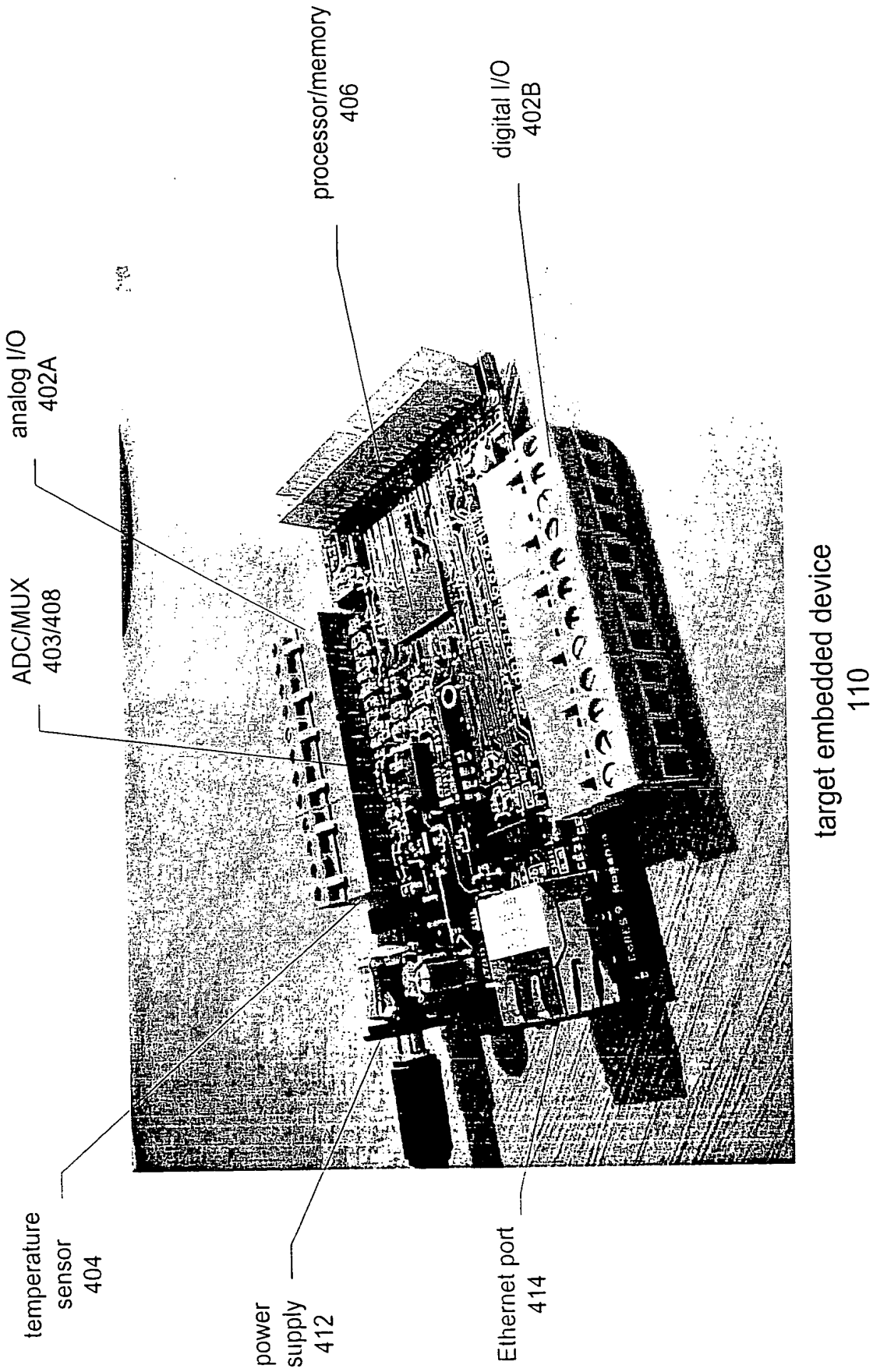
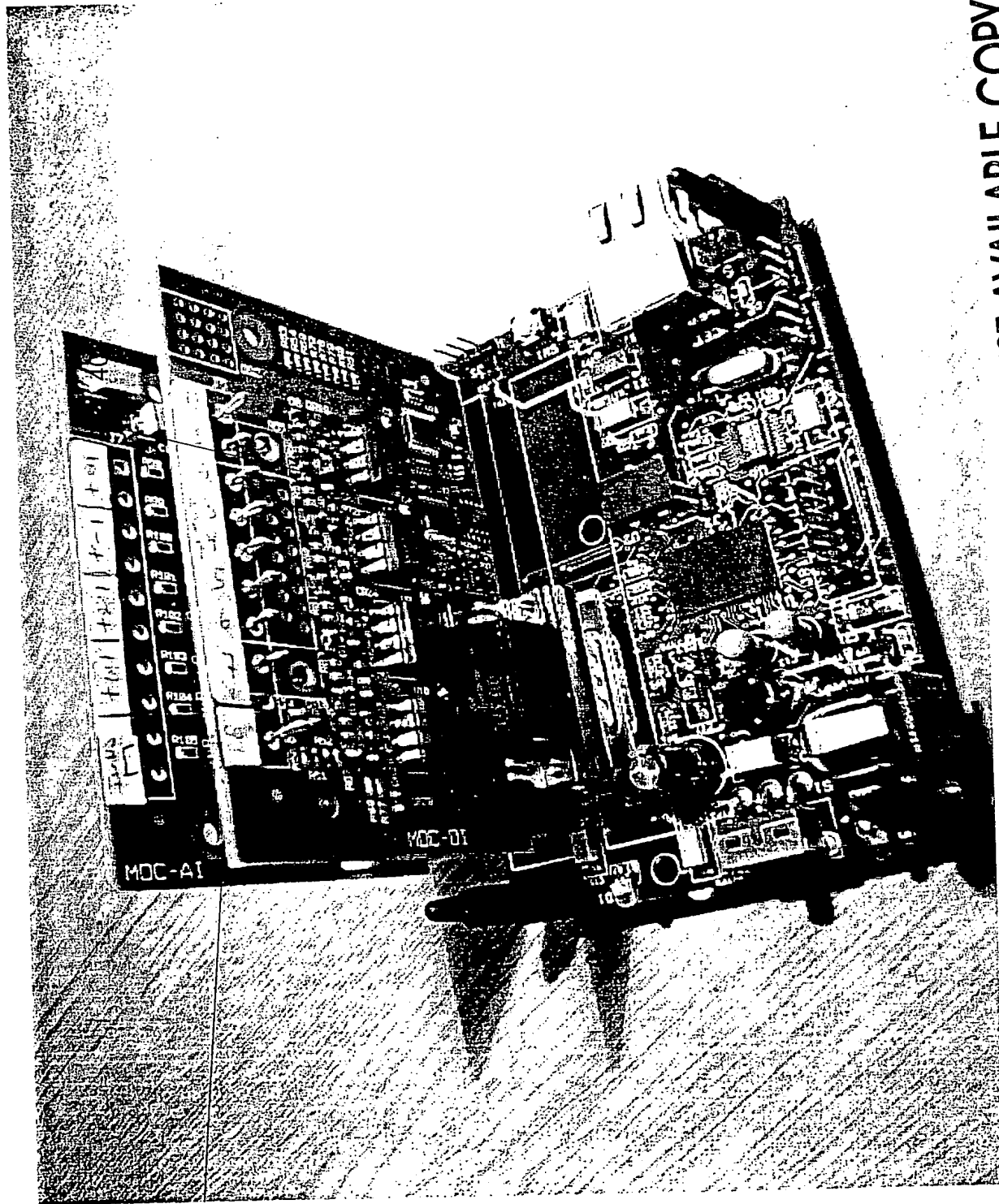


FIG. 4B

BEST AVAILABLE COPY

I/O  
cards  
420



target embedded device  
110A

FIG. 4C

BEST AVAILABLE COPY

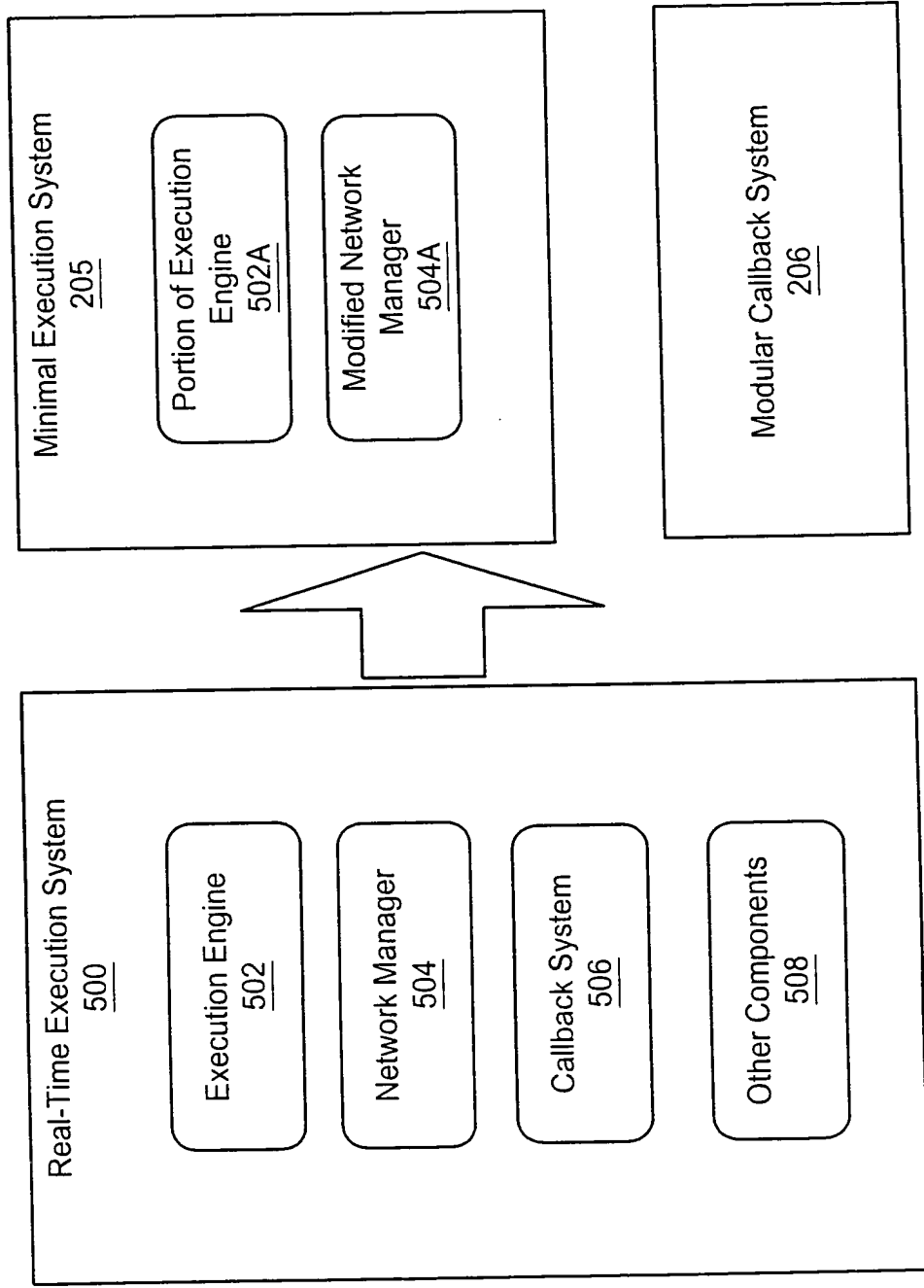


FIG. 5



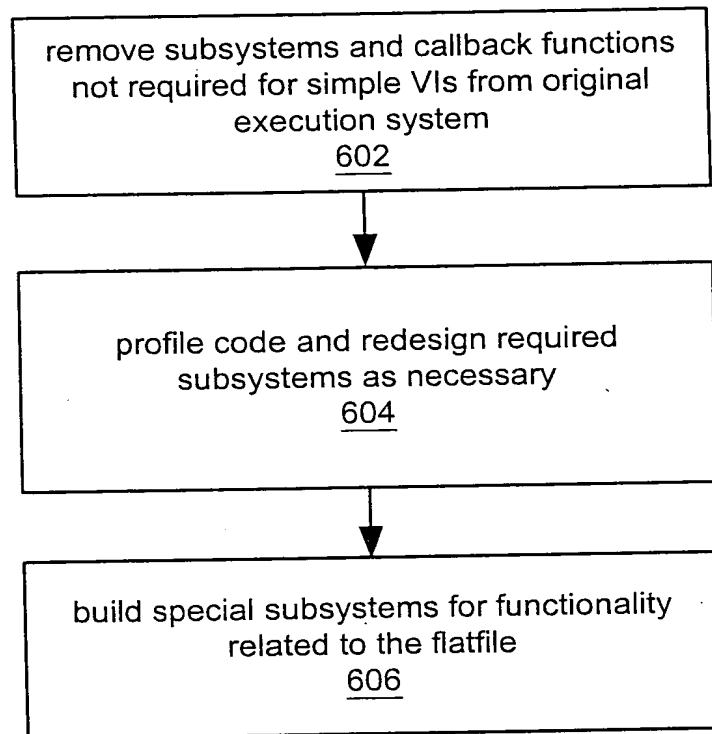


Figure 6

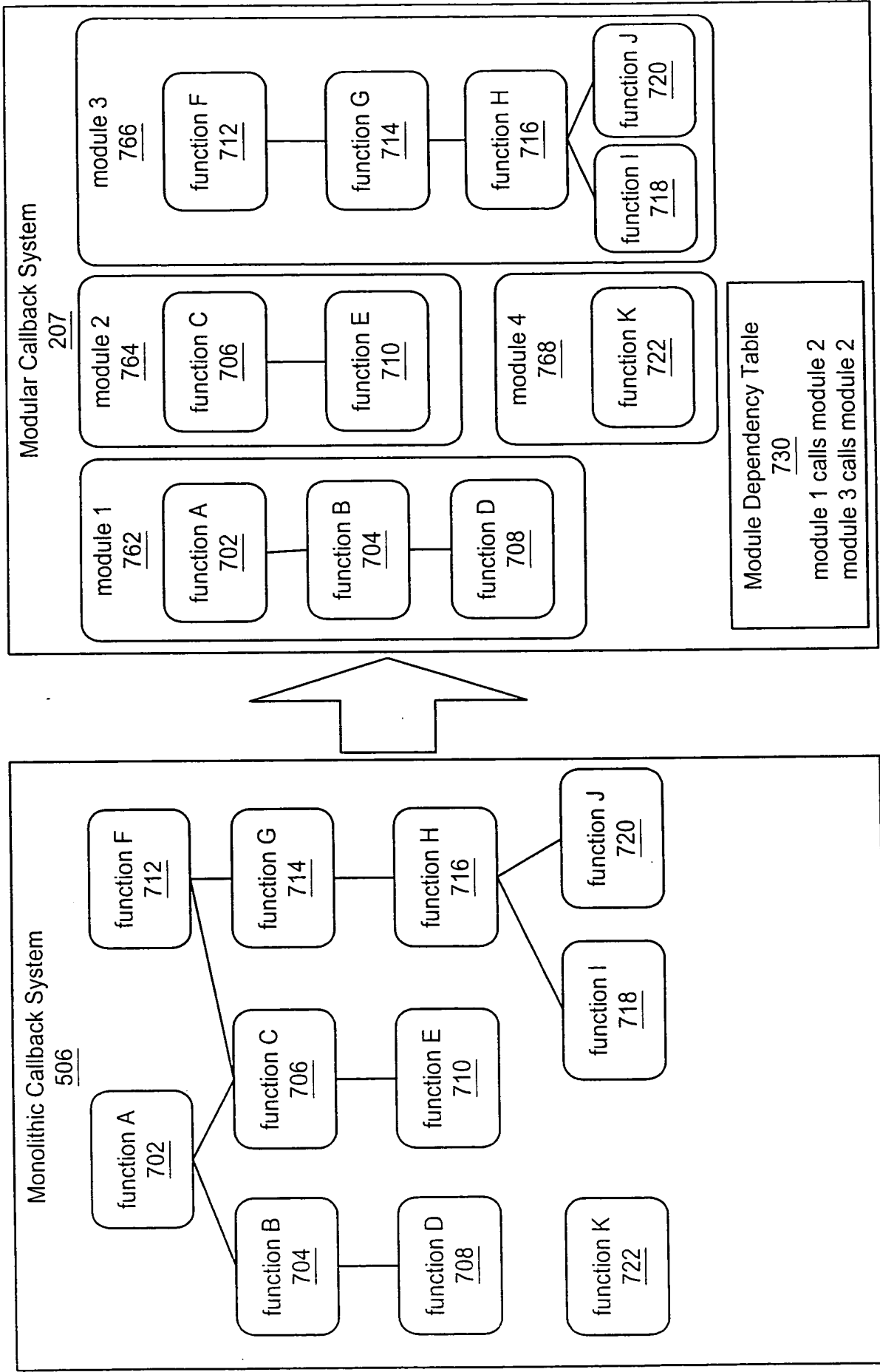


FIG. 7

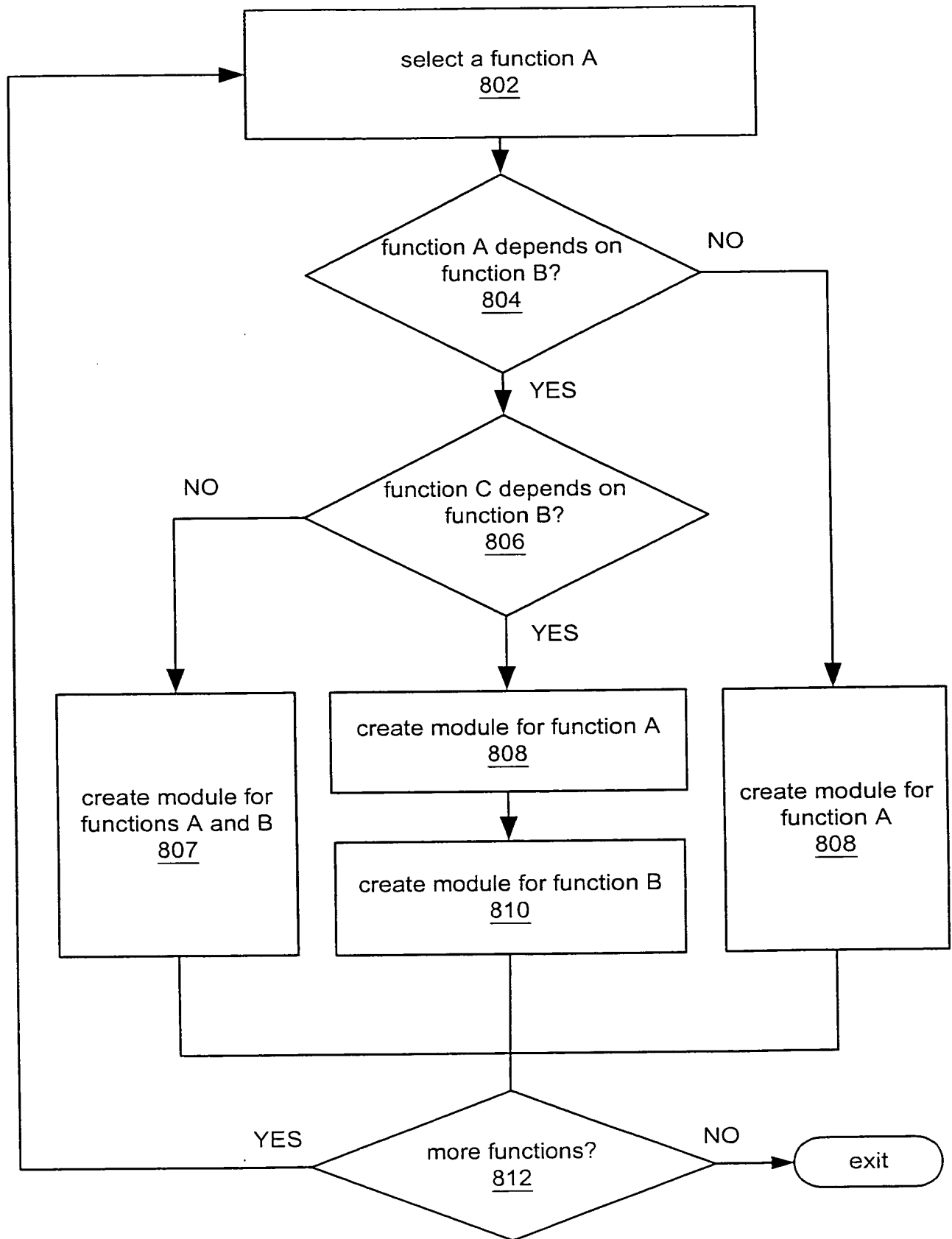


Figure 8

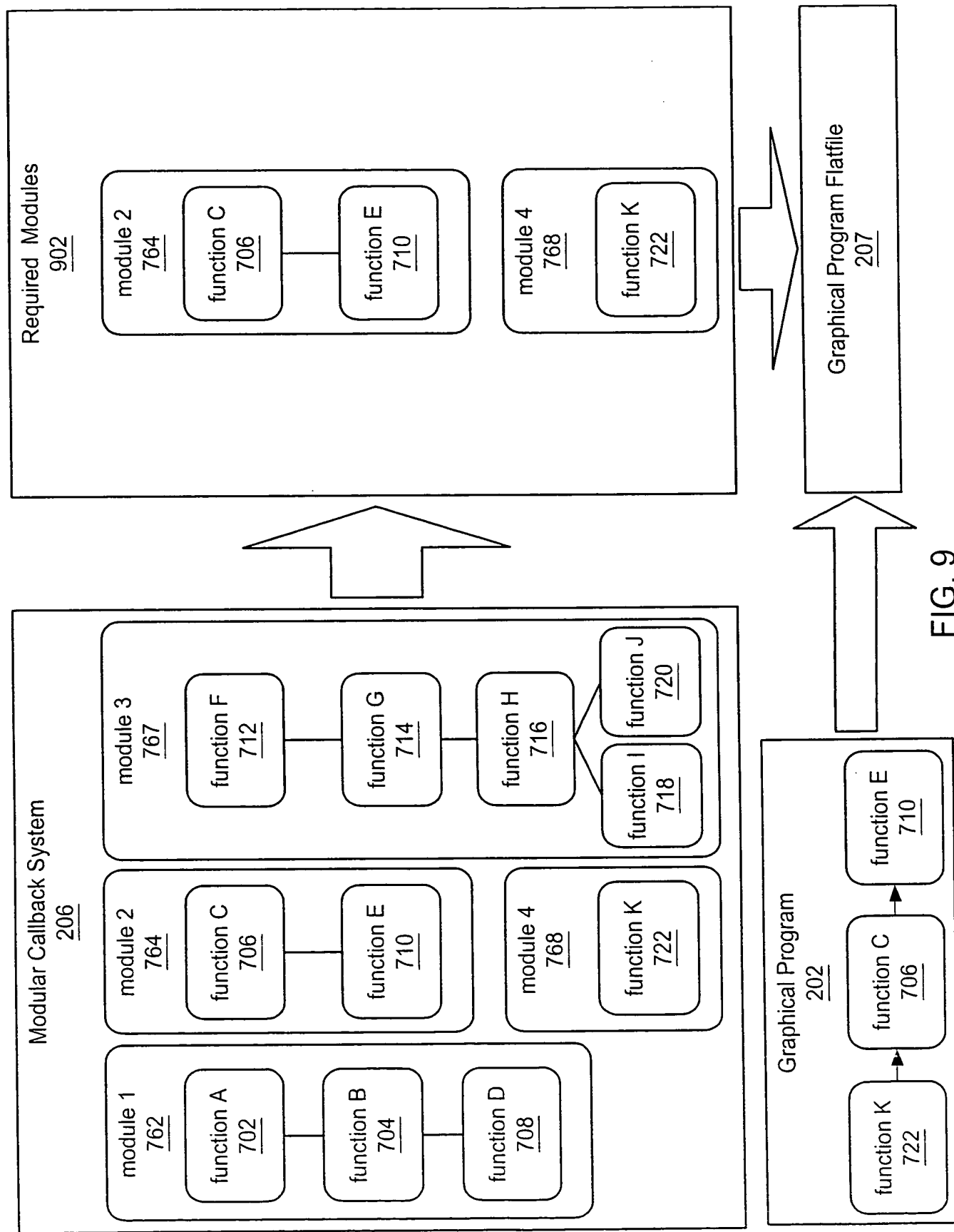


FIG. 9

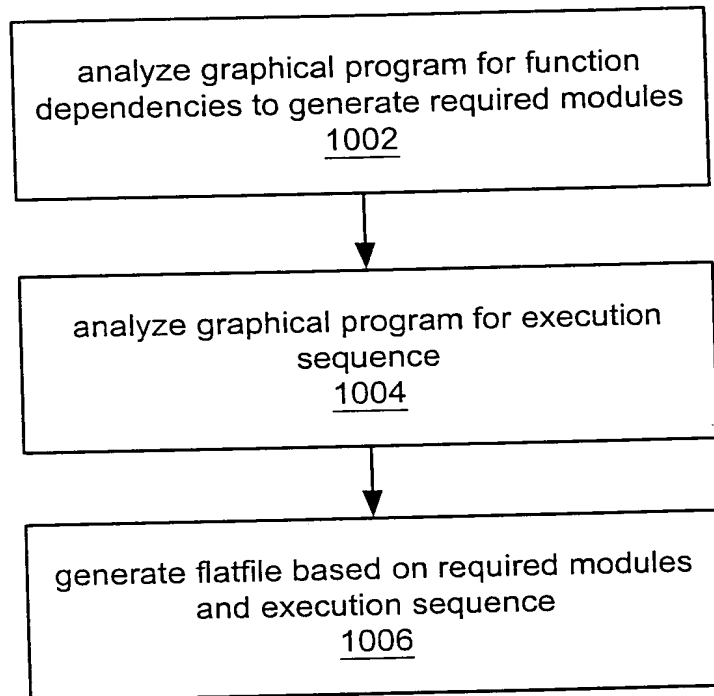


Figure 10

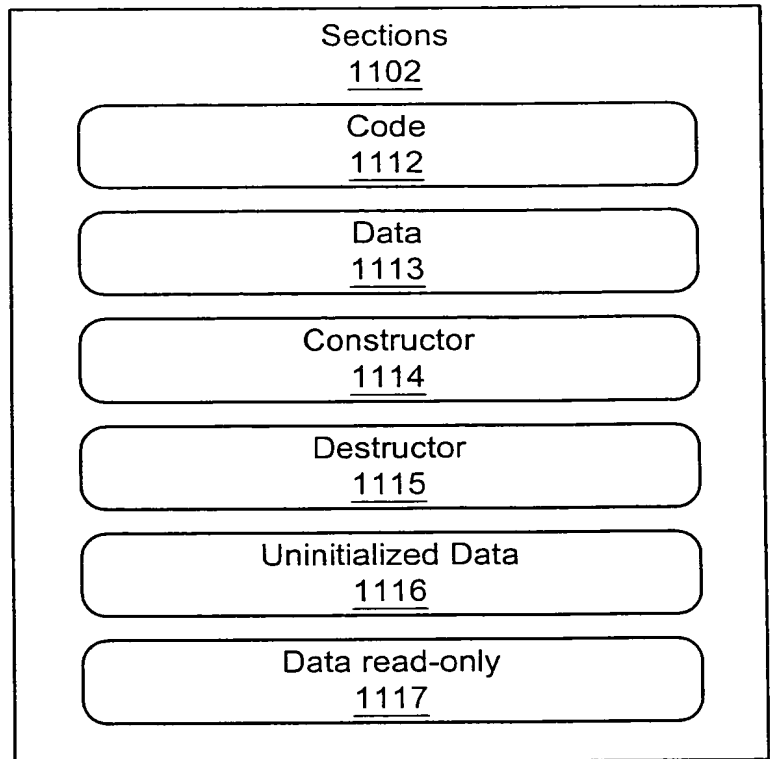
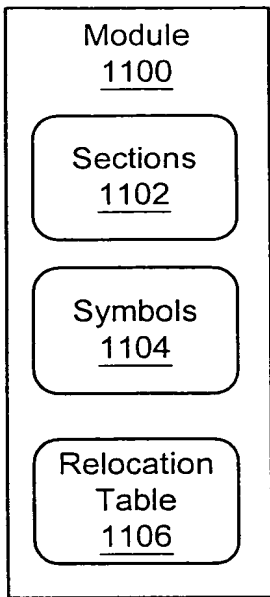
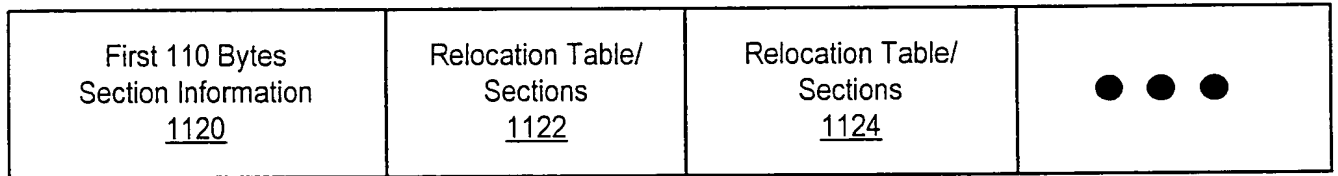


FIG.  
11A



Flatfile  
207

FIG.  
11B

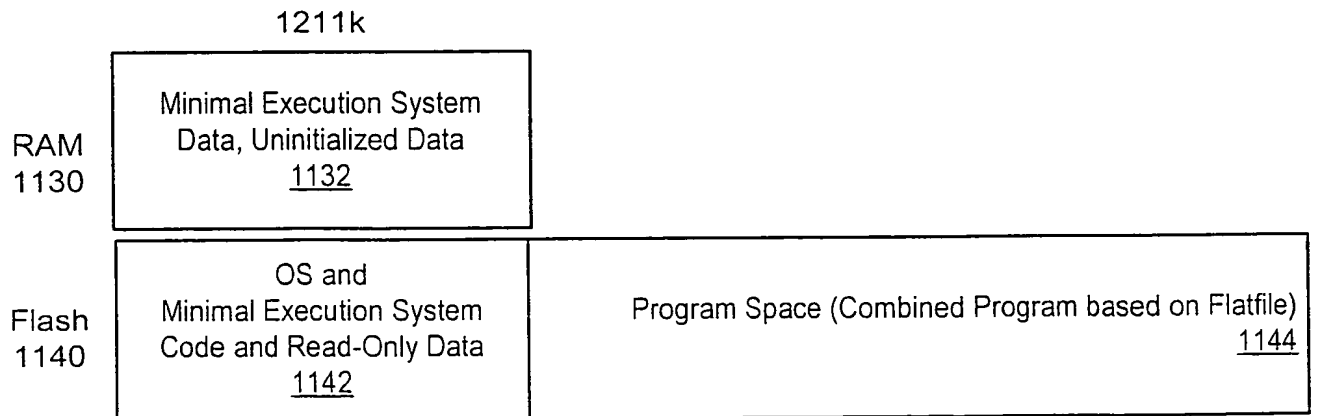


FIG.  
11C

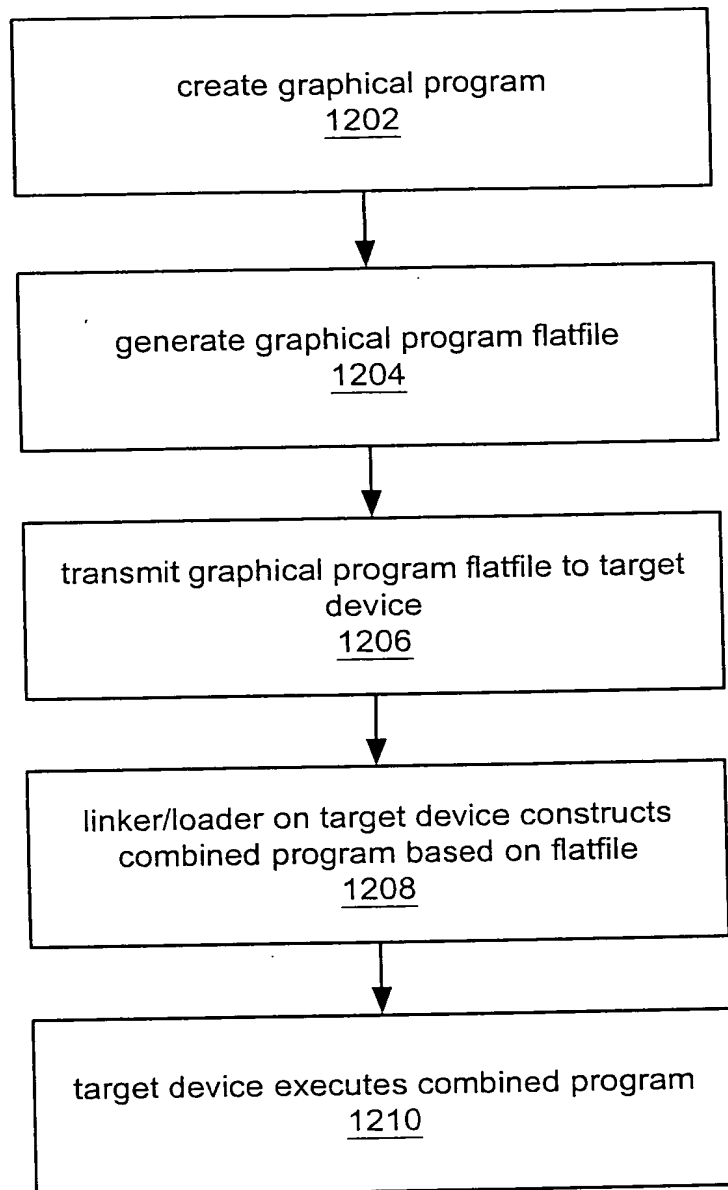
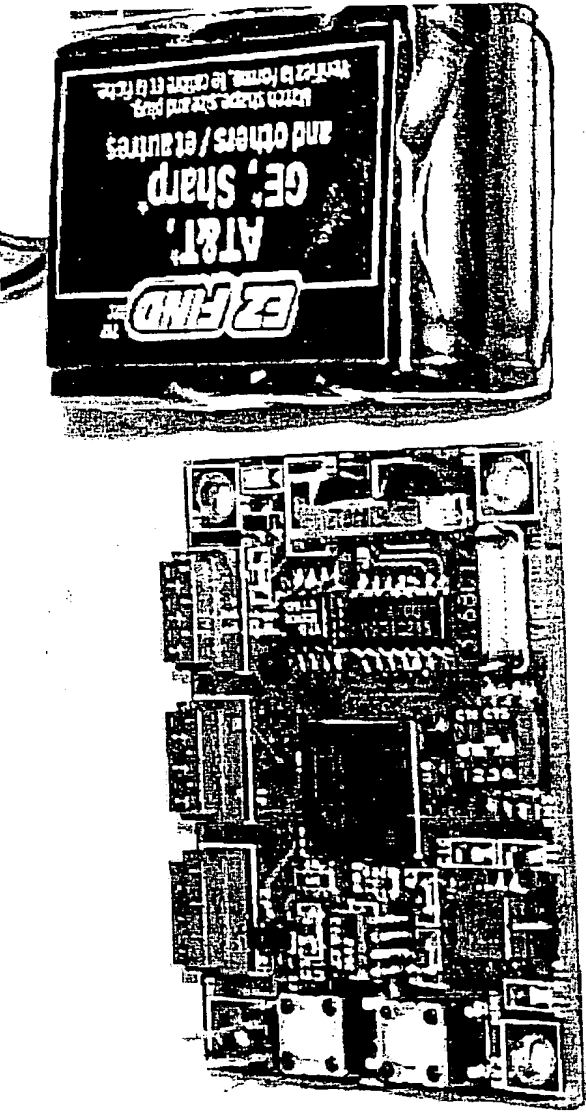


Figure 12

FIG. 13A

— YOUR MOST RELIABLE PARTNER





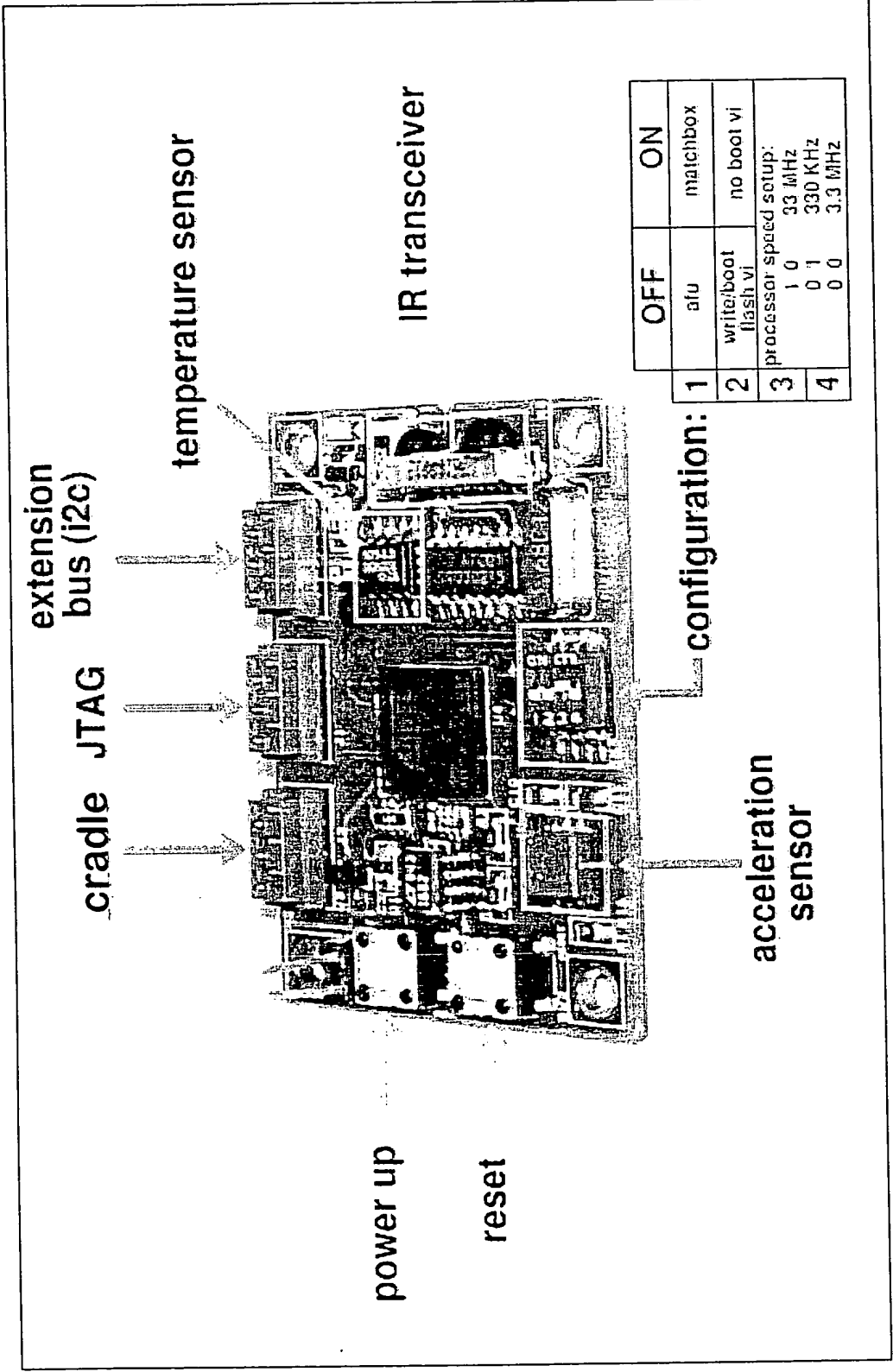


FIG. 13B

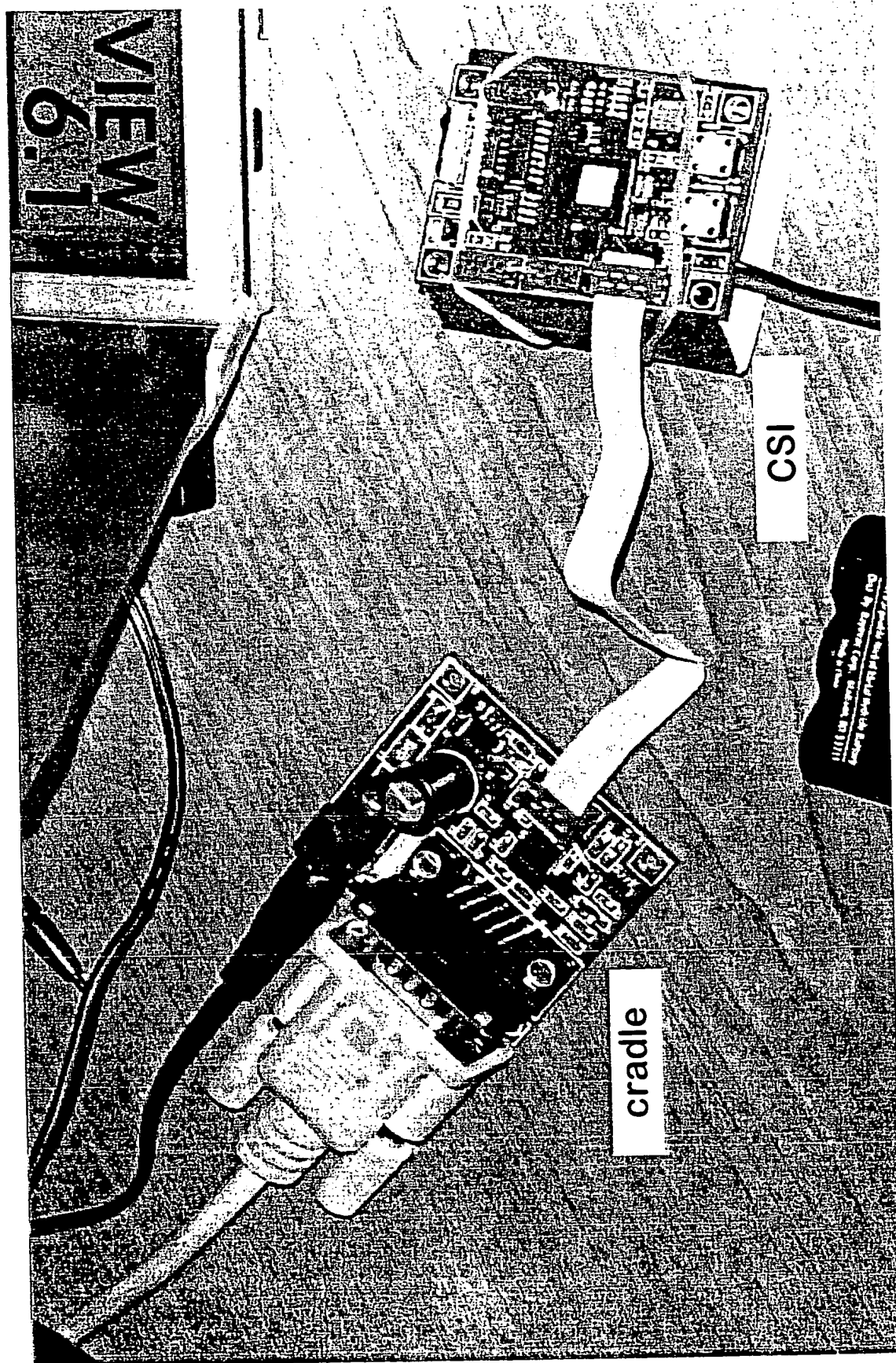


FIG. 14

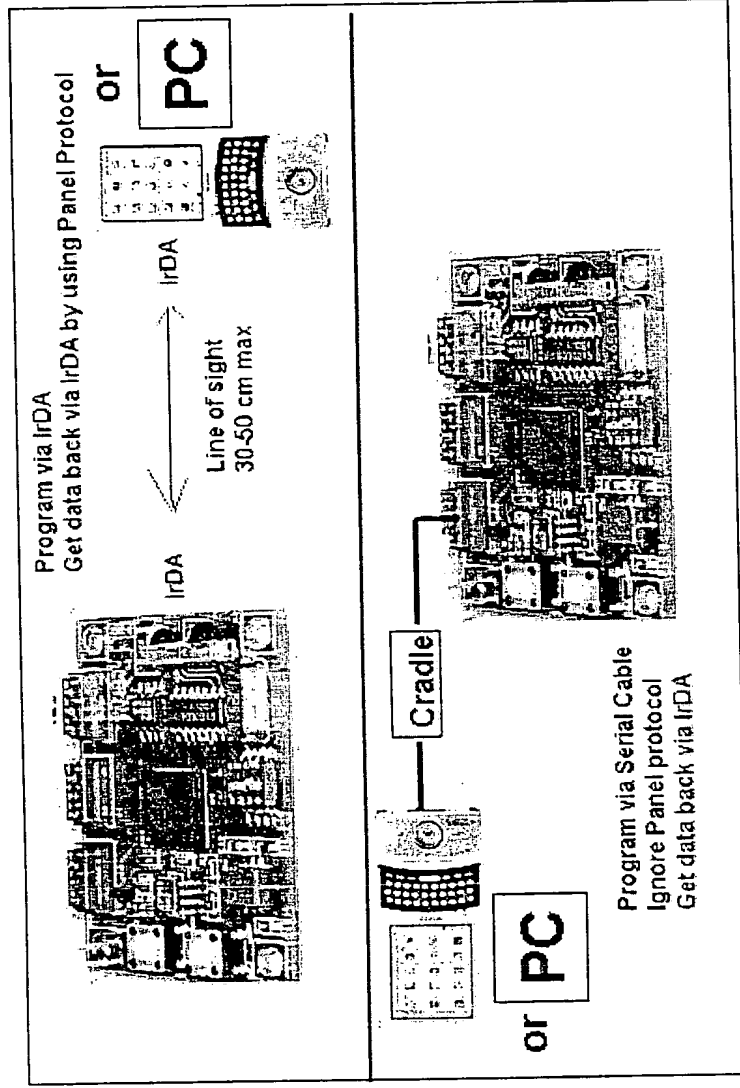


FIG. 15

Plane	Size (Bytes)	
A	8K	afw matchbox symbols (9151 bytes)
A	8K	
A	8K	
A	8K	
A	8K	
A	8K	
A	8K	
A	8K	matchbox (141224 bytes)
A	8K	
A	8K	
A	8K	
A	8K	
A	8K	
A	8K	
B	8K	running VI's (read-only sections)
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	stored flat object
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	
B	8K	

Figure 16

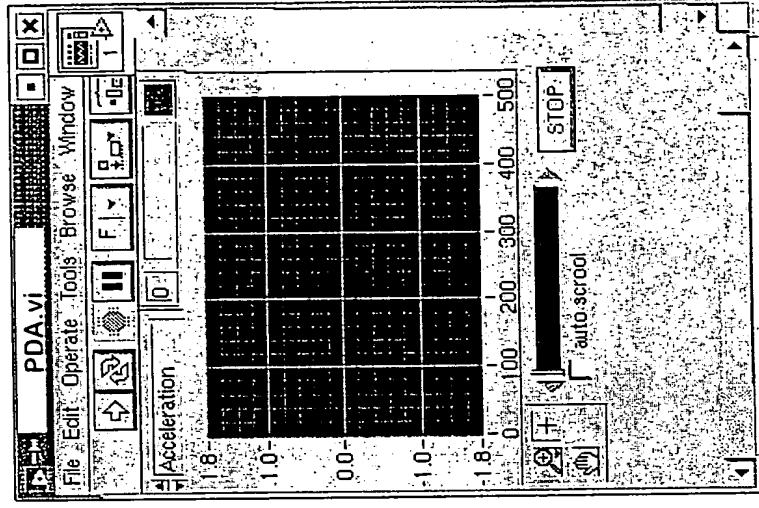


FIG. 18

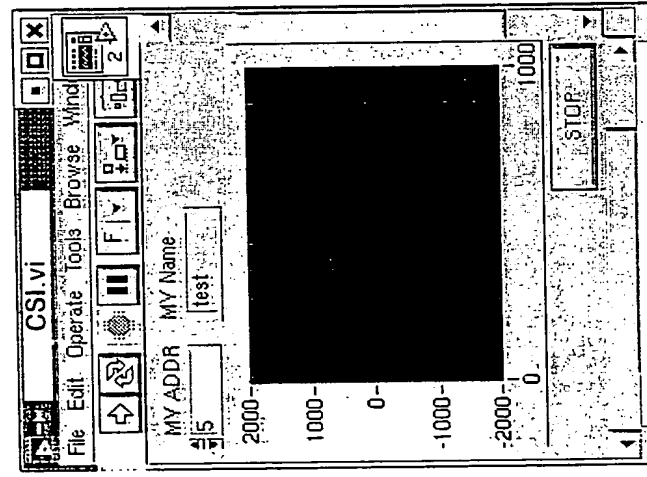


FIG. 17

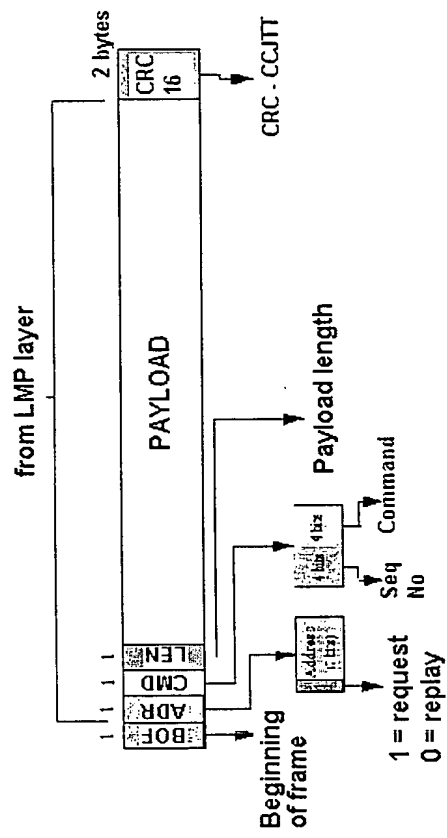
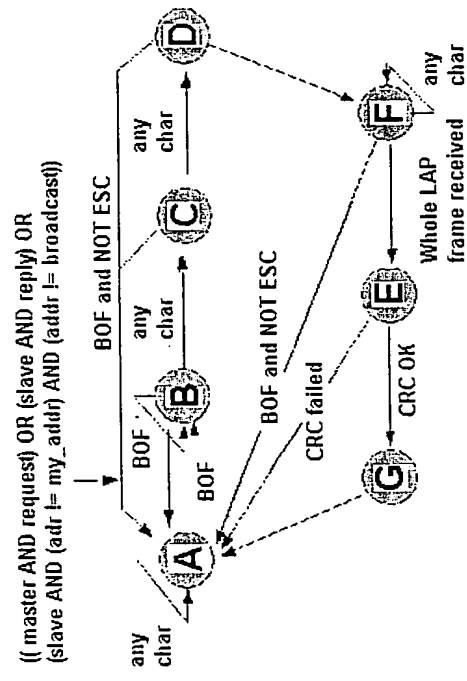


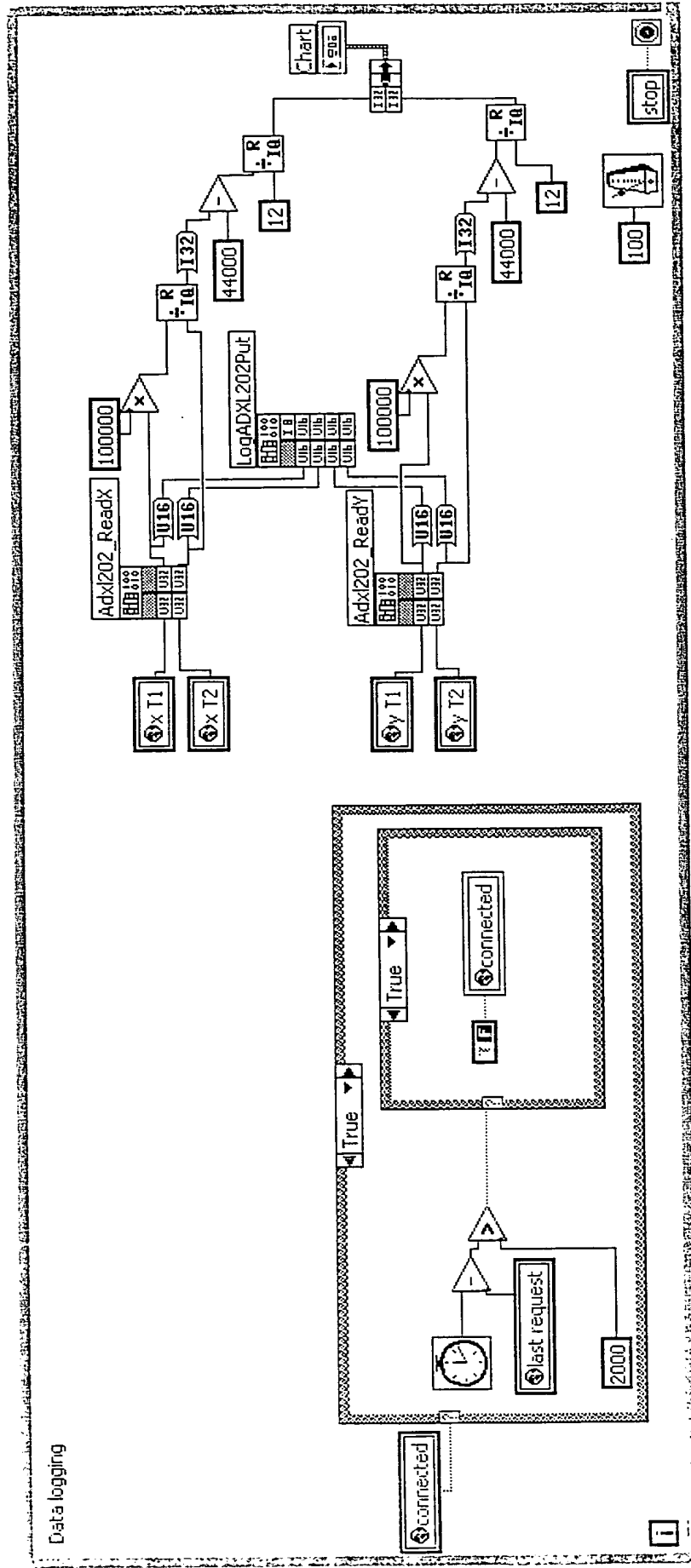
FIG. 19



States:

- A. wait for BOF
- B. read address
- C. read command
- D. read payload length
- E. receive payload
- F. check CRC
- G. store frame in LPM receive queue

FIG. 20



Front Panel Protocol communication from CBSI to PDA front panel chart

FIG. 21



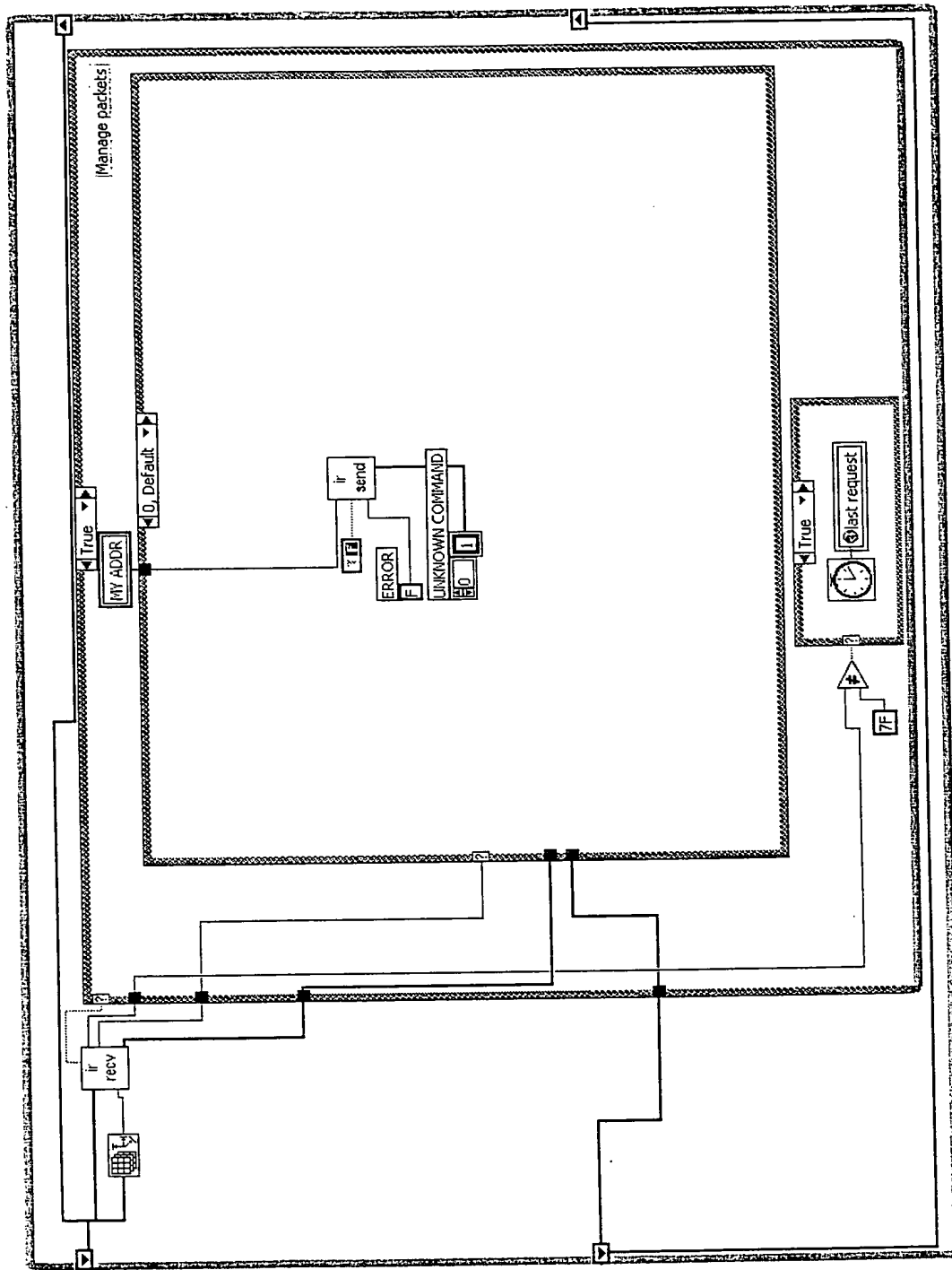


FIG. 22A

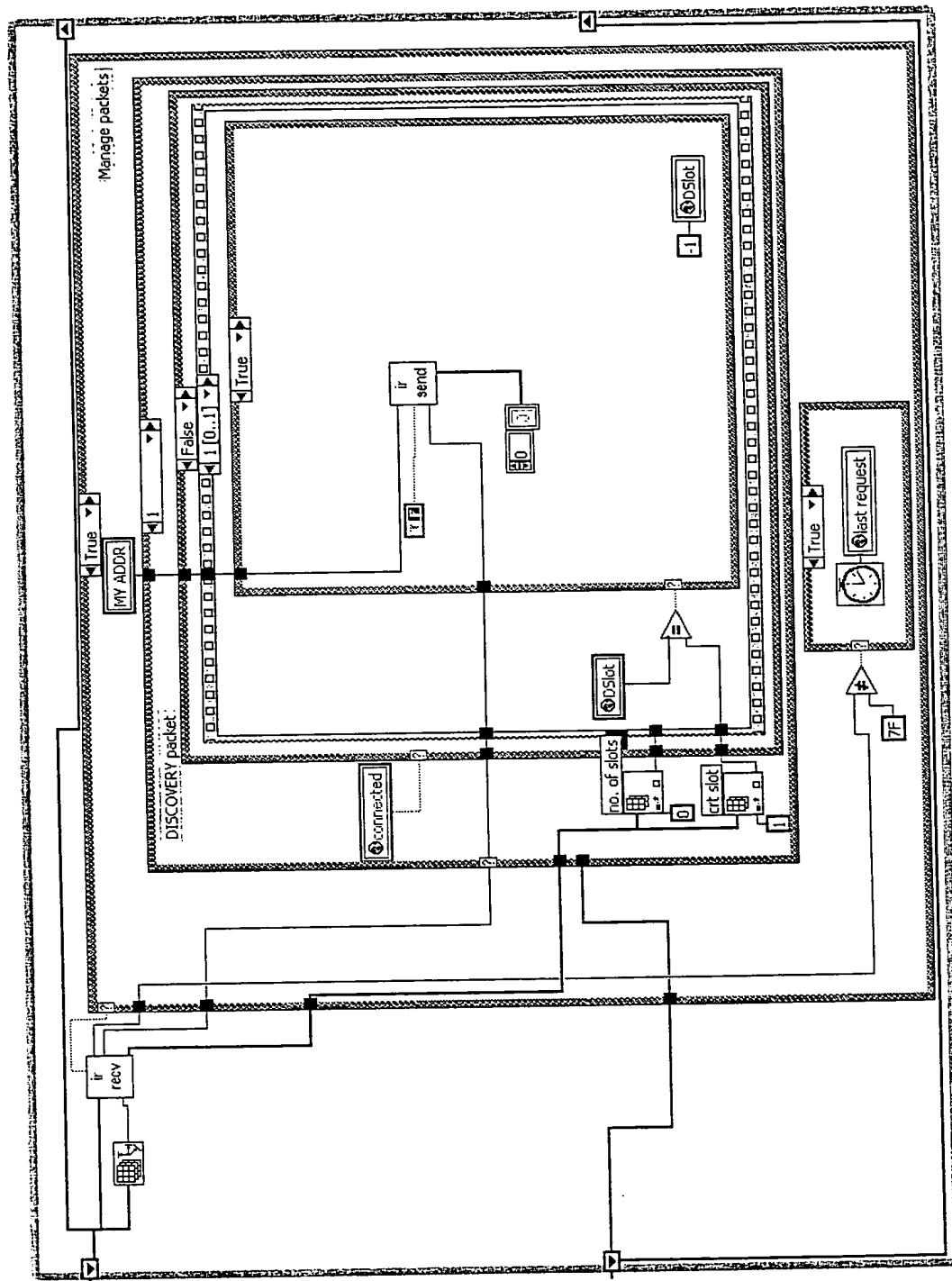


FIG. 22B

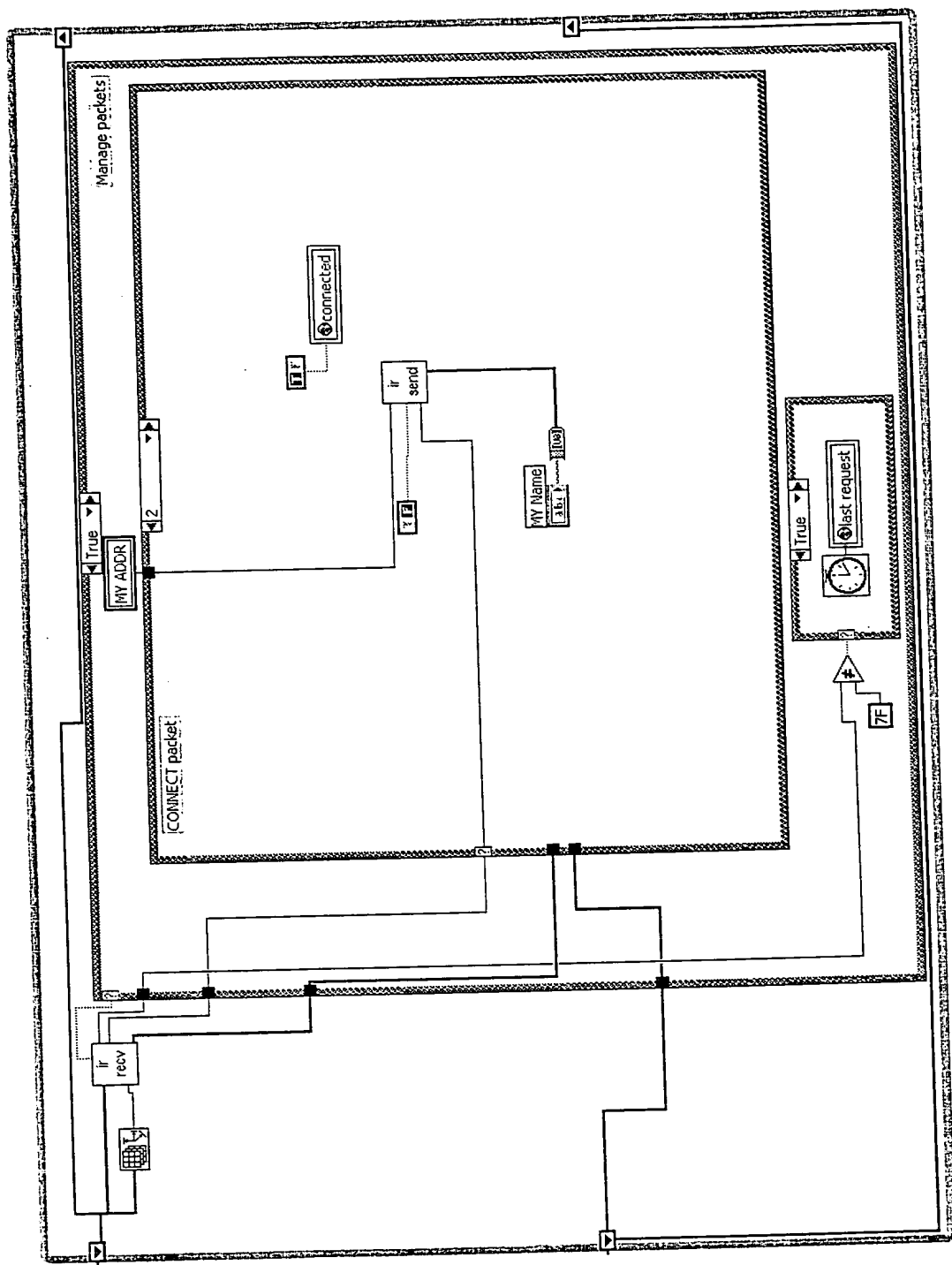


FIG. 22C

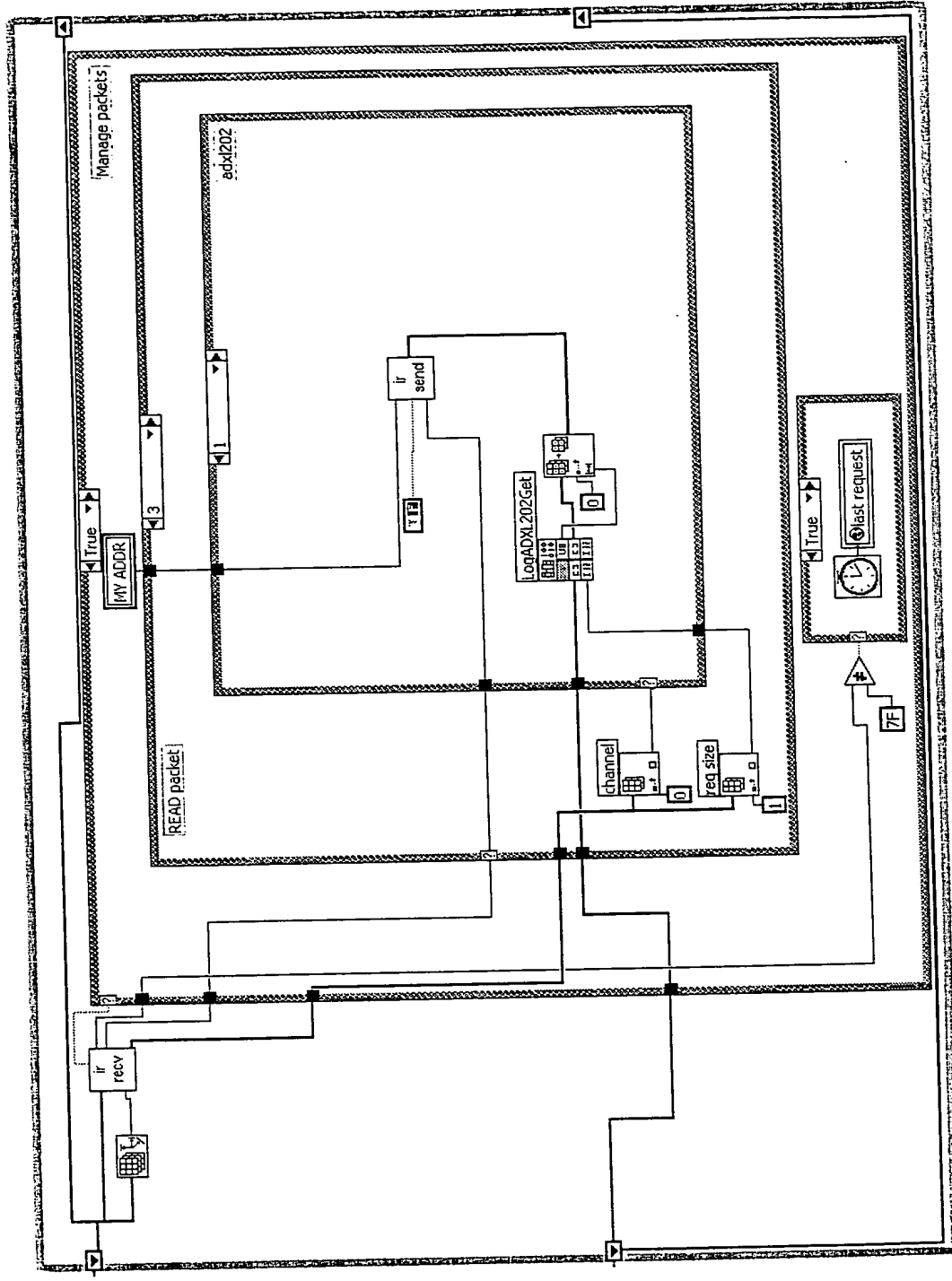


FIG. 22D

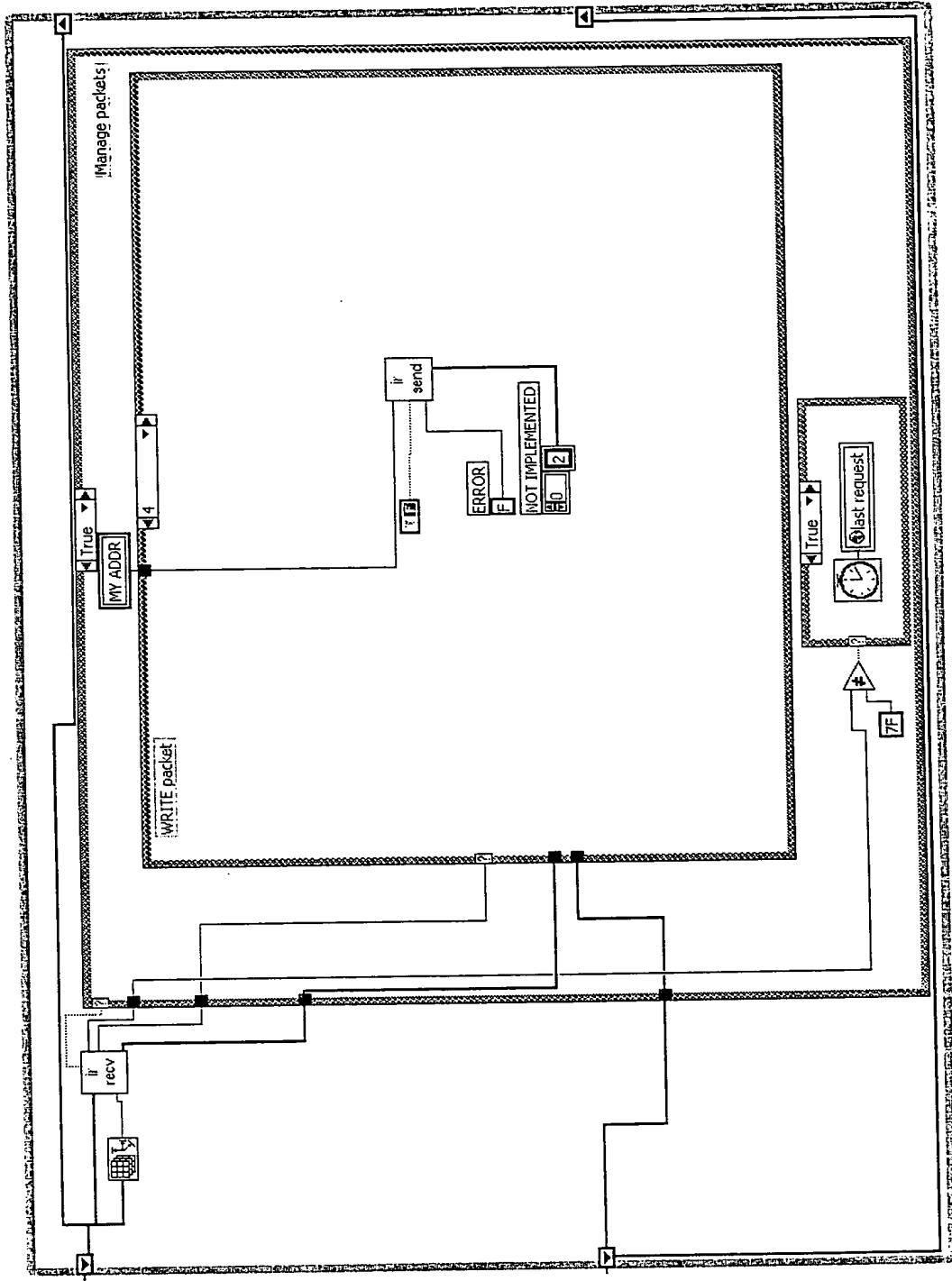


FIG. 22E

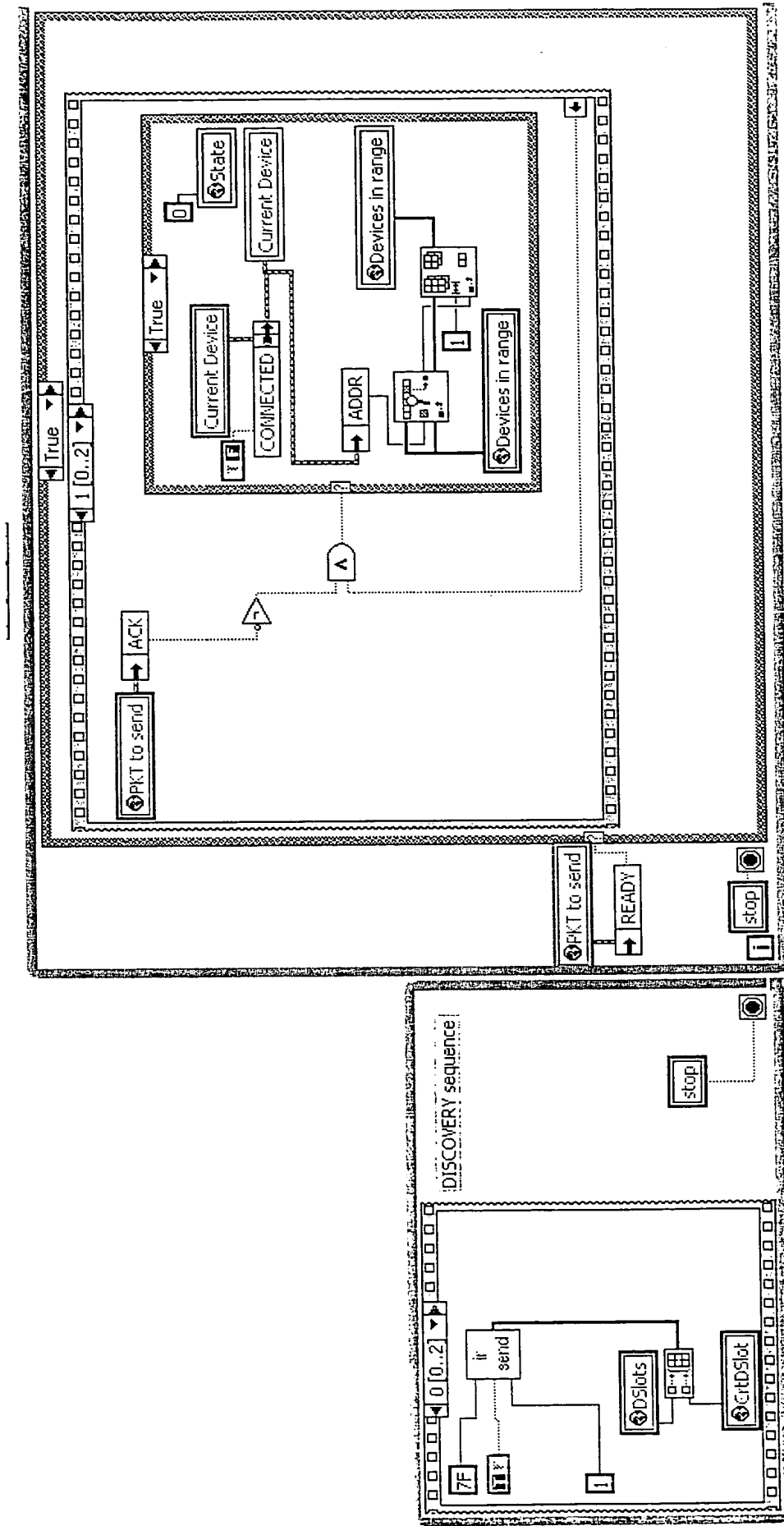


FIG. 23A

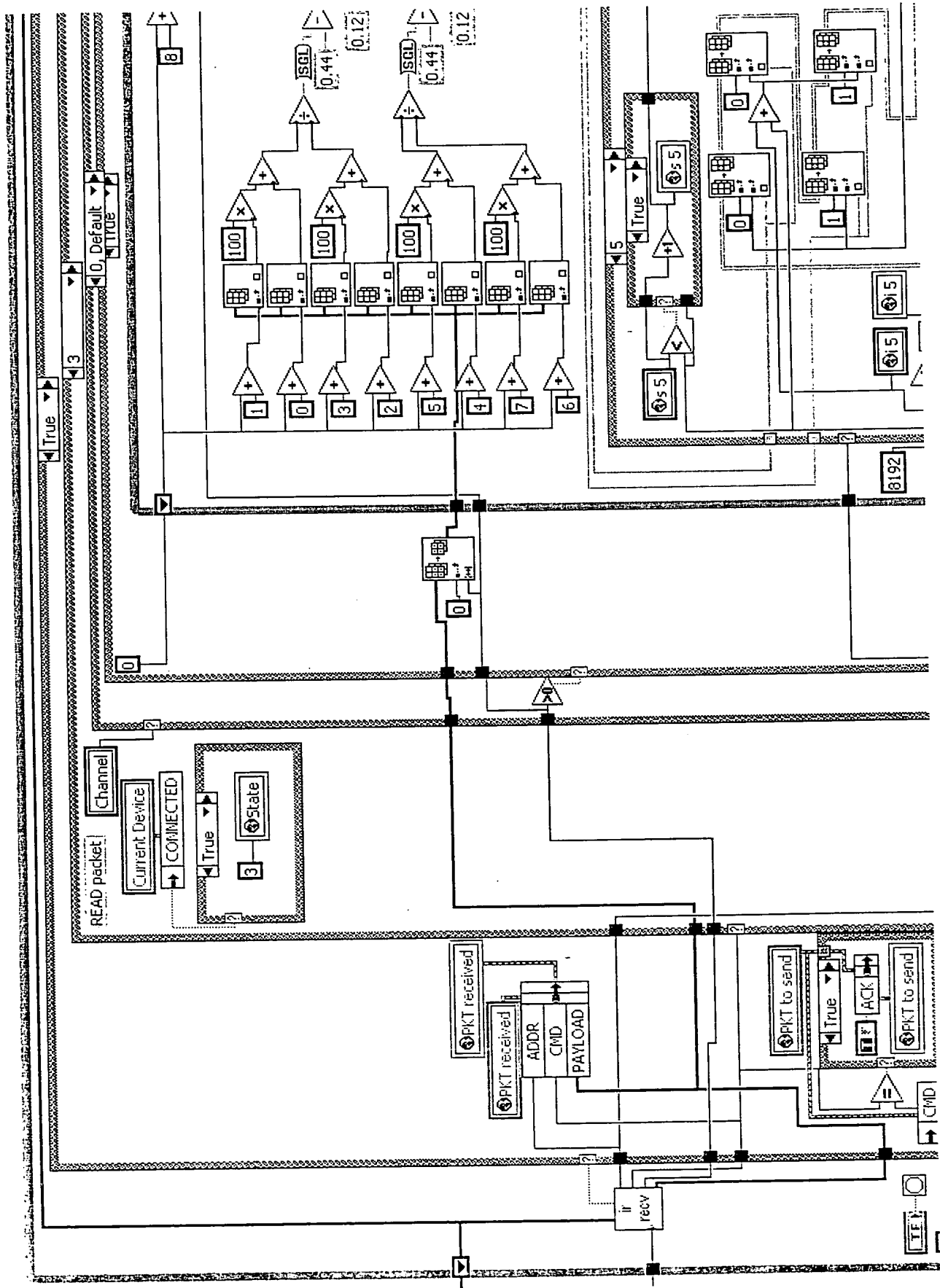


FIG. 23B

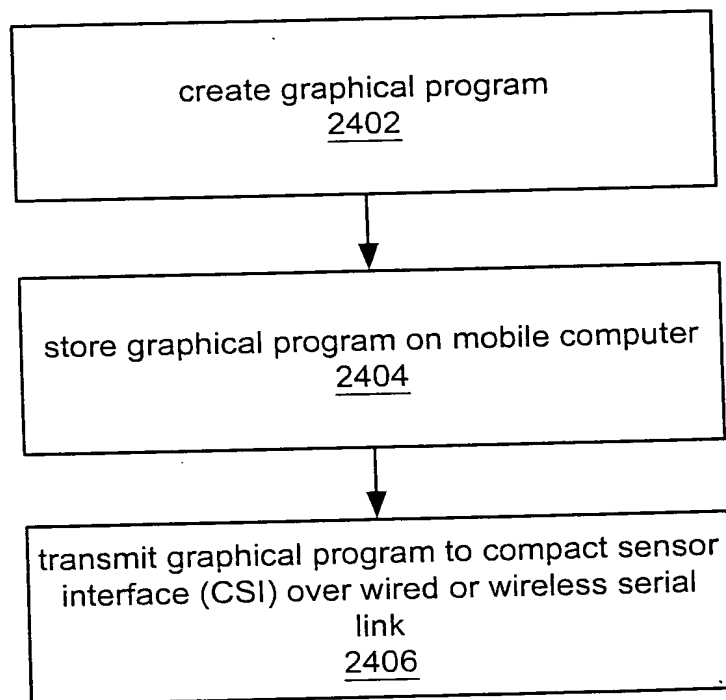


Figure 24



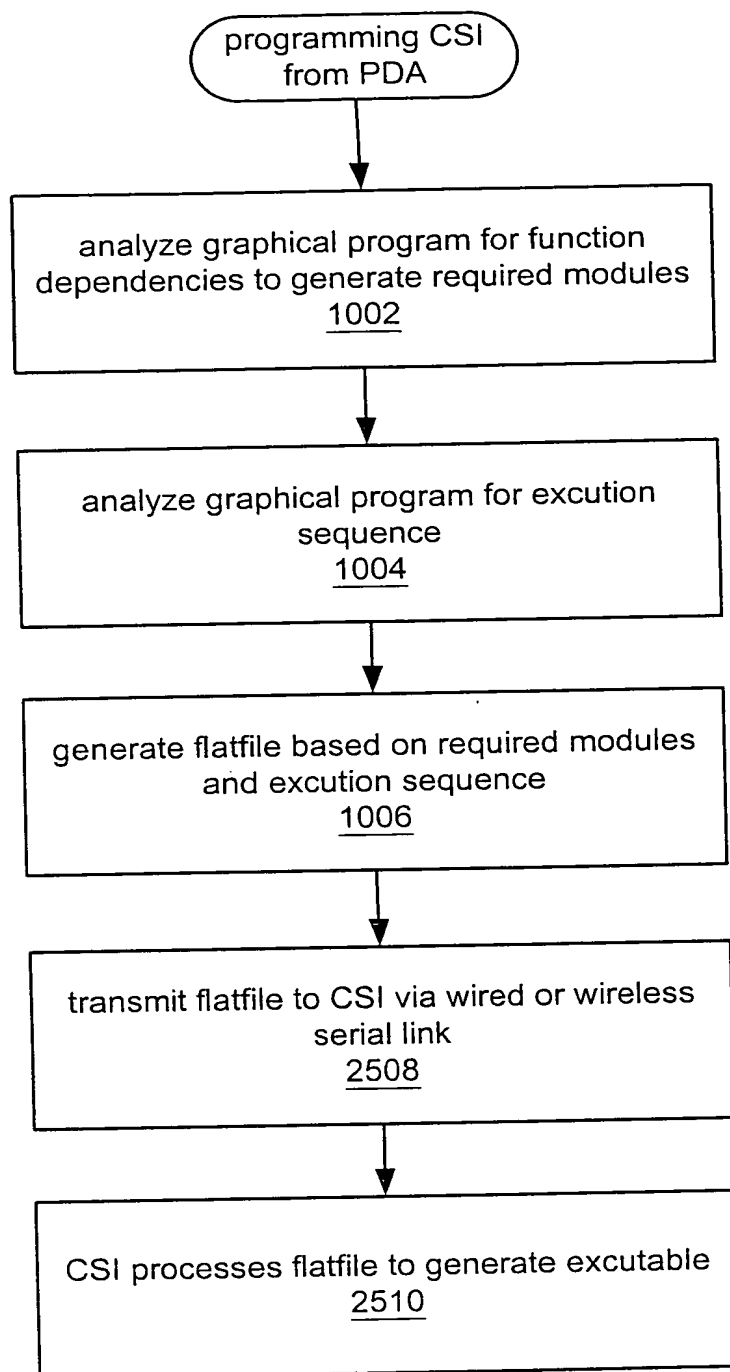


Figure 25

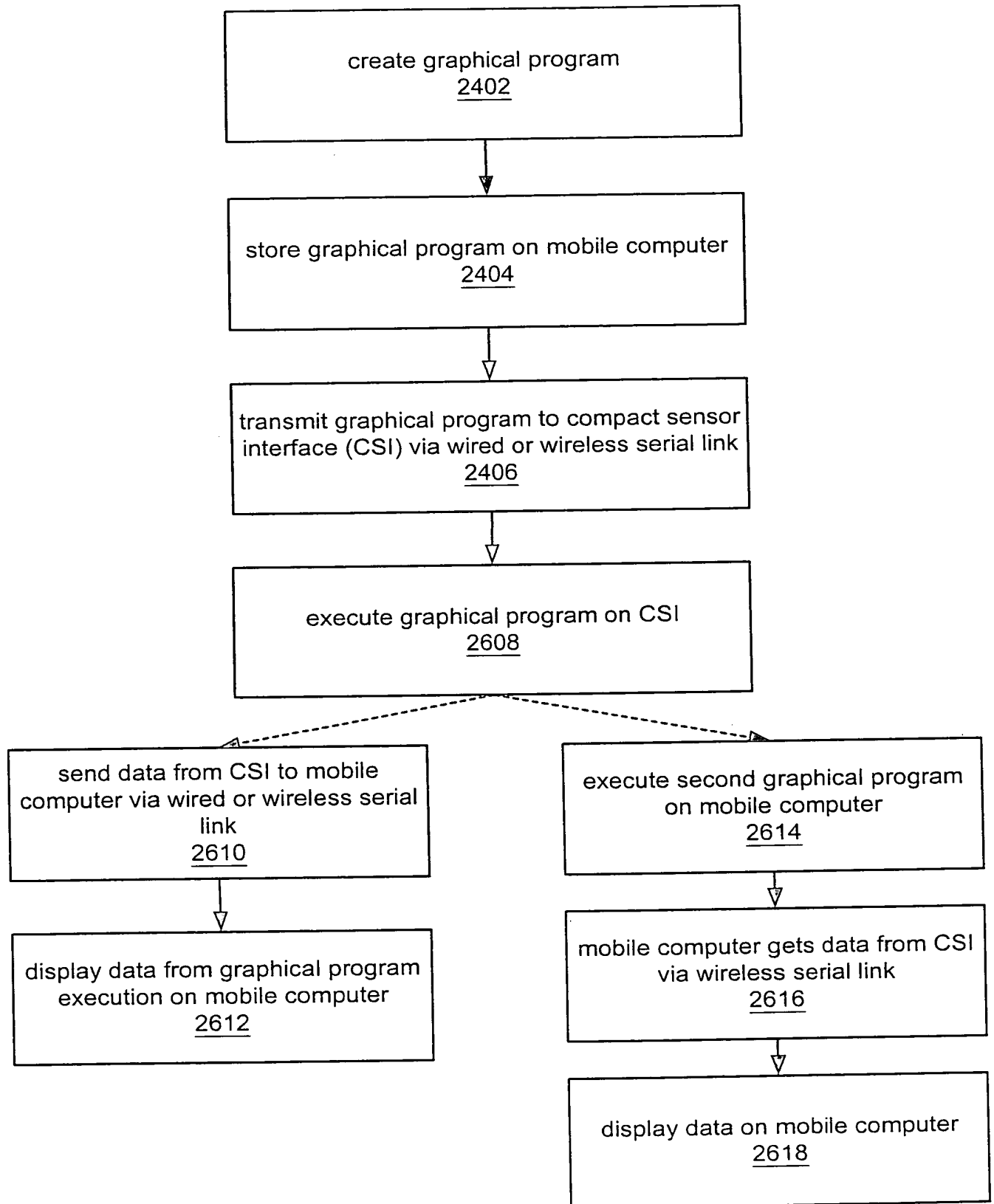


Figure 26